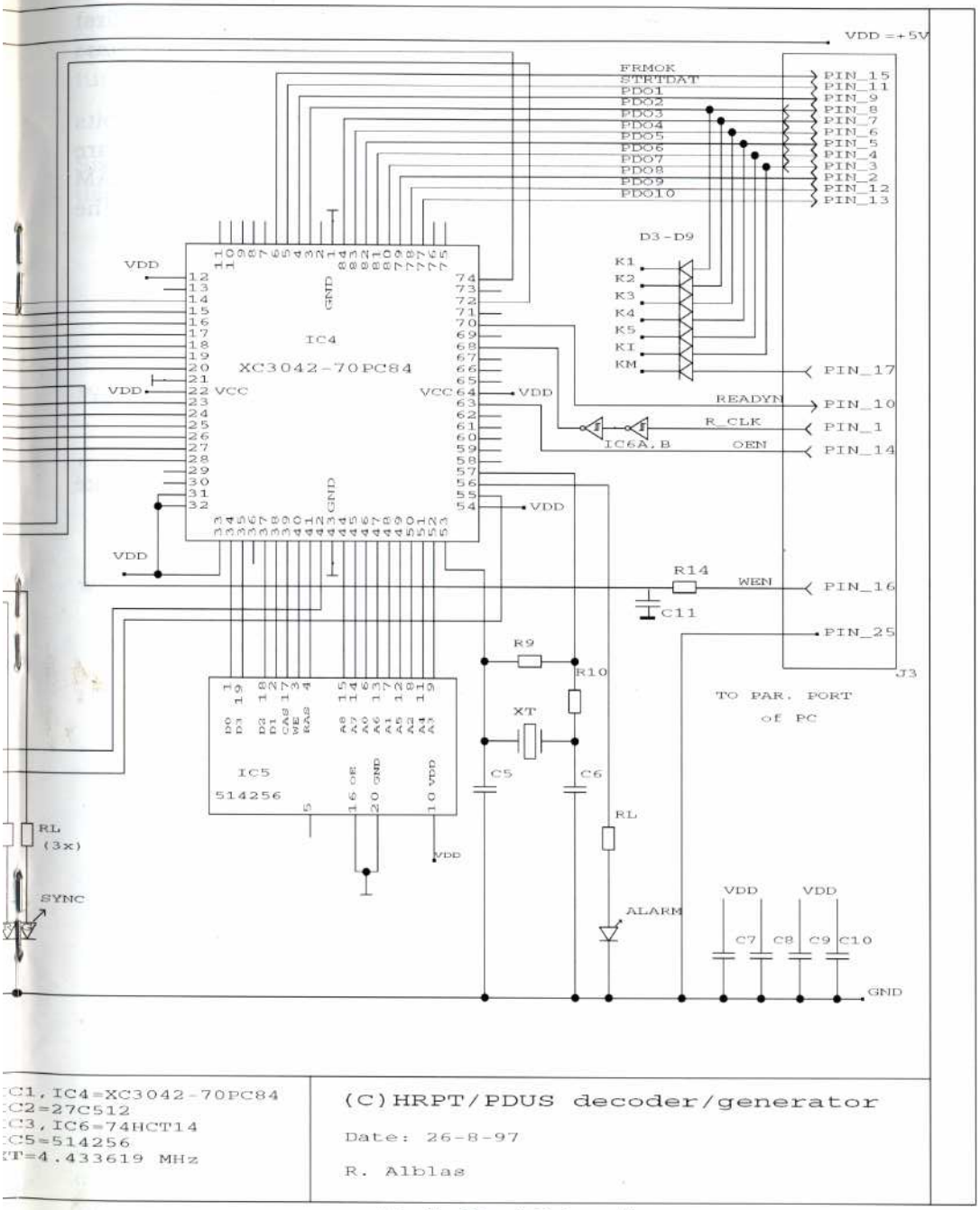

Fig.9 - Circuit Schematic