

The programs are free, and available from my web pages, where there is also a manual for the DOS program.

<http://www.alblas.demon.nl/wsat>

More information about SSI can be found at:

<http://www.tmcsys.com/ssi/ssifaq.html>

More Hardware Information

My home page also contains a lot of information about the hardware:

- * A PCB design, to make your own PCB
- * The code to load into the EPROM
- * Component list
- * Some addresses where to get the FPGA's
- * Schematics and PCB designs for simple (C)HRPT generators

Kits are not available, but it is possible to order a PCB and a programmed EPROM.

Note 1

Manufacturer: Xilinx (XC3042, XC3042A) or Lucent (3042).

Package: PLCC84

Max. frequency should be at least 70 MHz.

(Xilinx types are coded: XC3042-<freq><package_type>.)

Note 2

Trimmer C2 should be adjusted on a free-running VCO frequency of 10.646 MHz. Best is to measure at pin 76 of IC1; the frequency should be 665.4 kHz on this pin.

Note 3

Switches S1-S5, SI and SM may be left out; channel selection is normally done by software. In that case switch (or jumper) MAN should be open.

These switches are only needed if channel selection is desired without the PC connected. In this case, switch MAN should be closed.

Two images produced using this decoder may be seen on the outside back cover of this Journal, a NOAA 14 HRPT image of the Mediterranean Region from Wil Pfeifer, and a true-colour Feng Yun 1C image of the Sahara from Harry van Deursen.